



<b>Form PTO-1449 Modified</b>  List of Patent and Publications Cited by Applicant (Use several sheets if necessary)  U.S. Department of Commerce Patent and Trademark Office		Docket No. <b>MSFT-0742/177739.1</b>	Serial No. <b>Not Yet Assigned</b>
		Applicant <b>Nicholas P. Wilt, Sameer A. Nene, and Joseph S. Beda, III</b>	
		Filing Date <b>Herewith</b>	Group <b>Not Yet Assigned</b>
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>			
<i>SN</i>	AA	Bankston, J. et al., "Pipeline Processing with Modula-2," <i>J. Pascal Ada Modula 2</i> , 1986, 5(5), 46-51	
<i>SN</i>	AB	Nakatsuka, Y. et al., "A One Chip Super Graphics CPU with Direct Unified Memory Controller Suitable for Car Information and Control System," <i>Proceedings of the IEEE 2001 Custom Integrated Circuits Conference</i> , 2001, 421-423	
<i>SN</i>	AC	Randall, M., "Graphics IC Simplifies Design, Boosts Performance," <i>Computer Design</i> , 1986, 25(11), 73-77	
<i>SN</i>	AD	Williams, T., "Graphics Processor Boasts Virtual Memory," <i>Computer Design</i> , 1986, 25(7), 43-44	
EXAMINER <i>SN</i>		DATE CONSIDERED 7/7/05	

10/039036  
 01/04/02

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